

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	8	((("5778439") or ("5760602") or ("5036473") or ("5109353") or ("5140526") or ("5430687") or ("5500609") or ("5504440")).PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/08/07 14:15
S2	70	FPGA and ("shift register" same circular)	US-PGPUB; USPAT	AND	ON	2006/08/07 15:30
S3	201	712/10.ccls.	US-PGPUB; USPAT	AND	ON	2006/08/07 14:16
S4	172	712/37.ccls.	US-PGPUB; USPAT	AND	ON	2006/08/07 14:34
S5	627	712/227.ccls.	US-PGPUB; USPAT	AND	ON	2006/08/07 14:34
S6	1	FPGA and ("shift register" same circular same clb)	US-PGPUB; USPAT	AND	ON	2006/08/07 14:41
S7	37	S2 and selector	US-PGPUB; USPAT	AND	ON	2006/08/07 15:30
S8	175	("4642487"   "4700187"   "4703435"   "4706216"   "4722084"   "4724307"   "4744084"   "4758985"   "4768196"   "4786904"   "4815003"   "4829202"   "4849928"   "4868419"   "4908772"   "4935734"   "4949275").PN. OR ("5036473").URPN.	US-PGPUB; USPAT; USOCR	AND	ON	2006/08/07 15:34
S9	66	S8 and "shift register"	US-PGPUB; USPAT; USOCR	AND	ON	2006/08/07 16:11
S10	1220	(326/39).CCLS.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/08/07 16:11
S11	646	S10 and fpga	US-PGPUB; USPAT	AND	ON	2006/08/07 16:11
S12	1	tremblay.in. and fpga	US-PGPUB; USPAT	AND	ON	2006/08/07 16:12
S13	69	tremblay.in. and programmable	US-PGPUB; USPAT	AND	ON	2006/08/07 16:14
S14	32	(fpga or "programmable logic") and ((cache or cached or time-multiplexed or paged or overlay) same CLB)	US-PGPUB; USPAT	AND	ON	2006/08/07 16:47
S15	98	"time-multiplexed" same (fpga or "programmable logic")	US-PGPUB; USPAT	AND	ON	2006/08/07 16:51
S16	5	paged same (fpga or "programmable logic")	US-PGPUB; USPAT	AND	ON	2006/08/07 16:48
S17	19	"dynamically reconfigurable fpga"	US-PGPUB; USPAT	AND	ON	2006/08/07 17:02

## EAST Search History

S19	1	("5,646,545").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/08/07 16:57
S20	25	overlay with fpga	US-PGPUB; USPAT	AND	ON	2006/08/07 17:02
S21	269	partition\$2 with fpga	US-PGPUB; USPAT	AND	ON	2006/08/08 17:37
S22	52	partition\$2 with clb	US-PGPUB; USPAT	AND	ON	2006/08/08 17:57
S23	9	selecting near2 CLB	US-PGPUB; USPAT	AND	ON	2006/08/08 17:42
S24	20	S22 and selector	US-PGPUB; USPAT	AND	ON	2006/08/08 17:58
S25	32	S22 and (CLB same multiplex\$2)	US-PGPUB; USPAT	AND	ON	2006/08/08 17:58
S26	8	((("5778439") or ("5760602") or ("5036473") or ("5109353") or ("5140526") or ("5430687") or ("5500609") or ("5504440")).PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2007/03/22 16:45
S27	218	712/10.ccls.	US-PGPUB; USPAT	AND	ON	2007/03/22 16:45
S28	182	712/37.ccls.	US-PGPUB; USPAT	AND	ON	2007/03/22 16:45
S29	674	712/227.ccls.	US-PGPUB; USPAT	AND	ON	2007/03/22 16:45
S30	91	FPGA and ("shift register" same circular)	US-PGPUB; USPAT	AND	ON	2007/03/22 16:45
S31	1	FPGA and ("shift register" same circular same clb)	US-PGPUB; USPAT	AND	ON	2007/03/22 16:45
S32	53	S30 and selector	US-PGPUB; USPAT	AND	ON	2007/03/22 16:45
S33	180	("4642487"   "4700187"   "4703435"   "4706216"   "4722084"   "4724307"   "4744084"   "4758985"   "4768196"   "4786904"   "4815003"   "4829202"   "4849928"   "4868419"   "4908772"   "4935734"   "4949275").PN. OR ("5036473").URPN.	US-PGPUB; USPAT; USOCR	AND	ON	2007/03/22 16:45
S34	67	S33 and "shift register"	US-PGPUB; USPAT; USOCR	AND	ON	2007/03/22 16:45
S35	1297	(326/39).CCLS.	US-PGPUB; USPAT; USOCR	OR	OFF	2007/03/22 16:45
S36	707	S35 and fpga	US-PGPUB; USPAT	AND	ON	2007/03/22 16:45

## EAST Search History

S37	1	tremblay.in. and fpga	US-PGPUB; USPAT	AND	ON	2007/03/22 16:45
S38	74	tremblay.in. and programmable	US-PGPUB; USPAT	AND	ON	2007/03/22 16:45
S39	35	(fpga or "programmable logic") and ((cache or cached or time-multiplexed or paged or overlay) same CLB)	US-PGPUB; USPAT	AND	ON	2007/03/22 16:45
S40	115	"time-multiplexed" same (fpga or "programmable logic")	US-PGPUB; USPAT	AND	ON	2007/03/22 16:45
S41	5	paged same (fpga or "programmable logic")	US-PGPUB; USPAT	AND	ON	2007/03/22 16:45
S42	22	"dynamically reconfigurable fpga"	US-PGPUB; USPAT	AND	ON	2007/03/22 16:45
S43	1	("5,646,545").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2007/03/22 16:45
S44	29	overlay with fpga	US-PGPUB; USPAT	AND	ON	2007/03/22 16:45
S45	300	partition\$2 with fpga	US-PGPUB; USPAT	AND	ON	2007/03/22 16:45
S46	54	partition\$2 with clb	US-PGPUB; USPAT	AND	ON	2007/03/22 16:45
S47	9	selecting near2 CLB	US-PGPUB; USPAT	AND	ON	2007/03/22 16:45
S48	20	S46 and selector	US-PGPUB; USPAT	AND	ON	2007/03/22 16:45
S49	32	S46 and (CLB same multiplex\$2)	US-PGPUB; USPAT	AND	ON	2007/03/22 16:45